

**AMENDMENTS TO THE CLAIMS**

Please **CANCEL** claims 46-52 without prejudice or disclaimer in favor of presentation of this subject matter in a continuation application.

The following is a complete list of all claims in this application.

1-52. (Cancelled)

53. (Previously Presented) A thin film transistor substrate for a liquid crystal display comprising:

a transparent insulating substrate;

a gate line disposed on the first insulating substrate;

a storage capacitor electrode disposed on the insulating substrate;

a gate insulating layer that covers the gate line and the storage capacitor electrode;

a semiconductor layer disposed on the gate insulating layer;

a data line that crosses the gate line and is disposed on the gate insulating layer;

a metal pattern that is disposed over the storage capacitor electrode, and is disposed on the same layer with the data line;

a passivation layer that comprises an organic insulating material and is disposed on the semiconductor layer and the data line, and has a contact hole that exposes the metal pattern; and

a pixel electrode connected to the metal pattern through the contact hole; wherein the passivation layer contacts a portion of the semiconductor layer between source and drain regions thereof.

54. (Previously Presented) The thin film transistor substrate of claim 53, wherein the dielectric constant of the passivation layer is in a range of about 2.4-4.7.

55. (Previously Presented) The thin film transistor substrate of claim 53, wherein the passivation layer has a flat surface.

56. (Previously Presented) The thin film transistor substrate of claim 53, wherein the pixel electrode overlaps at least a portion of the data line.

57. (Cancelled)